

Preimage search using low communication cost parallel Grover algorithm

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Introduction

Reversibility

Finding t -images

Example

Conclusion

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Preimage

Let H be a function that $H : \{0, 1\}^n \rightarrow \{0, 1\}^n$. Preimage search is given an output y , find a x such that $H(x) = y$.

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It is desirable that given an output it should be computationally infeasible to find any input that hashes to that output.

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Preimage

Consider $n = 128$ and $H = \text{AES}$ and 0 fixed as a plain text, i.e., $H(x) = \text{AES}_x(0)$, where x is a key.

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The complexity to find one key is 2^{128} guesses.

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Brute-force search for one preimage

Let H be a function that $H : \{0, 1\}^n \rightarrow \{0, 1\}^n$.

The brute force is to check every input x given an output y . The time complexity will be 2^n guesses using classical computers.

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If we apply Grover's algorithm, using a quantum computer, the complexity decreases to $2^{n/2}$ guesses.

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Brute-force search for multi target preimages

Let H be a function that $H : \{0, 1\}^n \rightarrow \{0, 1\}^n$.

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However, we have a set of output y 's, i.e., $Y = \{y_1, y_2, \dots, y_t\}$ and we want to find one y_i .

Now, we verify every input x with set of output Y . If we **ignore several costs**, the complexity decreases to $2^n/t$ guesses in a classical computer.

If we apply Grover's algorithm, using a quantum computer, the complexity decreases to $2^{n/2}/t^{1/2}$ guesses.

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Costs for comparison

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- ▶ Quantum computer:
 - ▶ Single target: $2^{n/2}$
 - ▶ Multi target: $t * 2^{n/2} / t^{1/2}$

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 - ▶ Each processor runs $2^{128}/pt$ fast steps, to find one of t independent AES keys k_1, \dots, k_t , using a fixed plain text, e.g, AES(0).

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- ▶ However, it is pre-quantum.

NIST has claimed that AES-128 is secure enough.

Introduction - Parallel rho method

Distinguish Point

Consider $H : \{0, 1\}^b \rightarrow \{0, 1\}^b$

Take x an input of H , $x' = H(x)$.

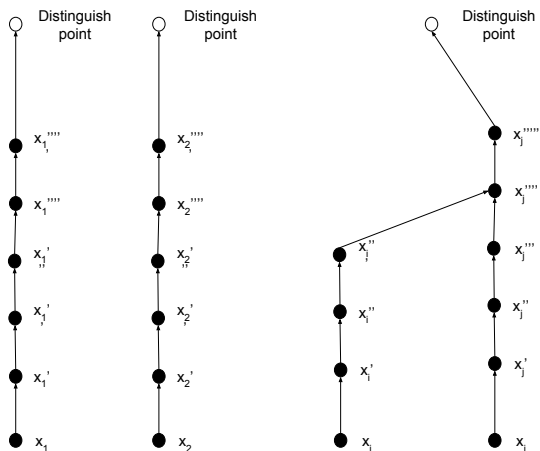
Thereafter, take x' and apply H again, $x'' = H(x')$.

It is possible to do it n times (H^n), until a given condition is satisfied. In our case, we want the first $0 < d < b/2$ bits as 0.

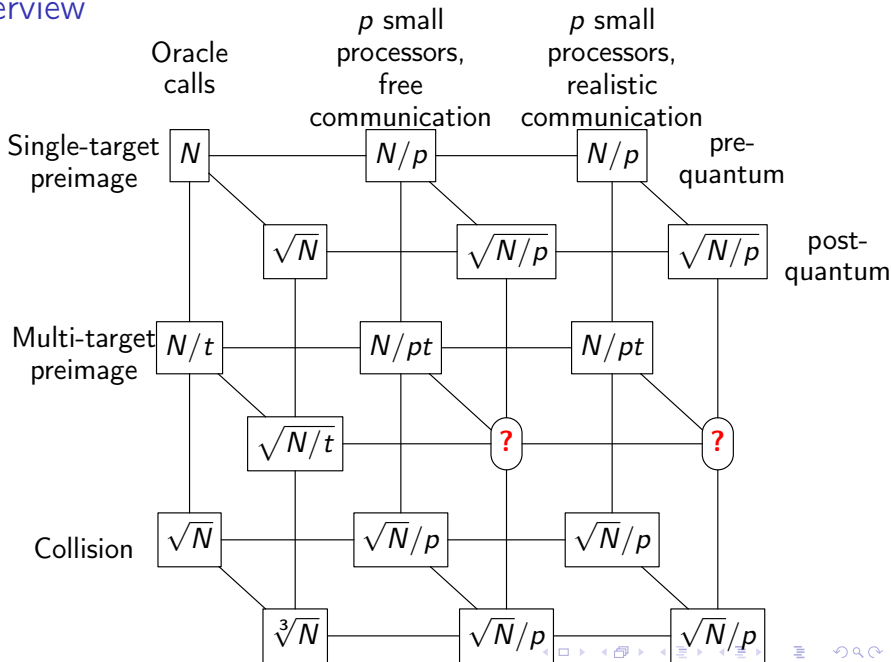
$H_d^n(x)$ means d bits of x , computed n times.

Introduction - Parallel rho method

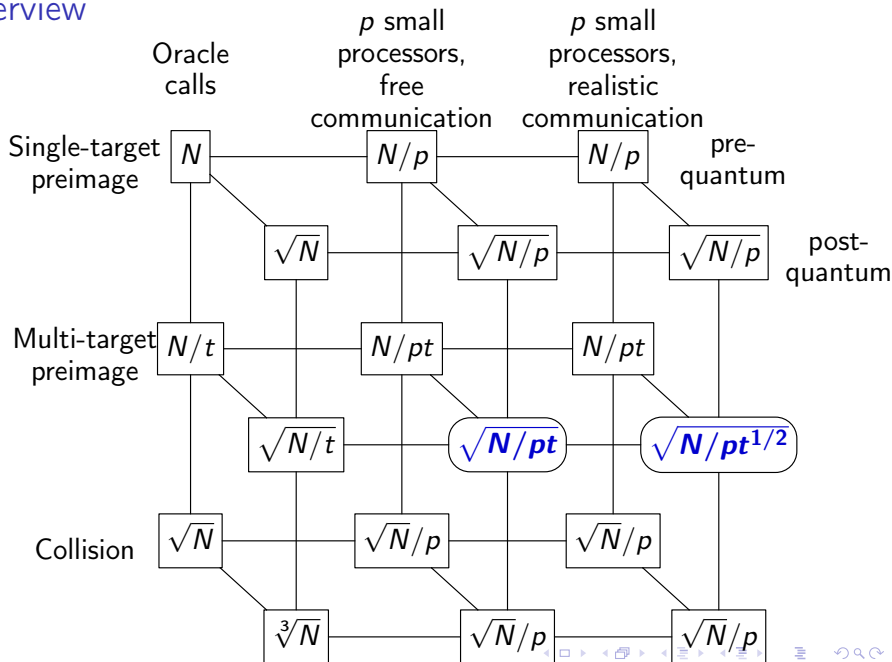
Distinguish Point



Overview



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Distinguish point in quantum setting

Distinguish point in quantum computers

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- ▶ It is not possible to design a “simple circuit” for distinguish point;
- ▶ The sorting needs to be reversible too.

Distinguish point in quantum setting

Using classical computers

Example to compute $H^3(x)$:

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time 0: x y

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time 0:	x	y	
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Example to compute $H^3(x)$:

time 0:	x	y	
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Trade-off from Bennett–Tomp

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time 0: x y 0 0 0

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time 0:	x	y	0	0	0
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time 4:	x	$y + H^3(x)$	$H(x)$	0	0

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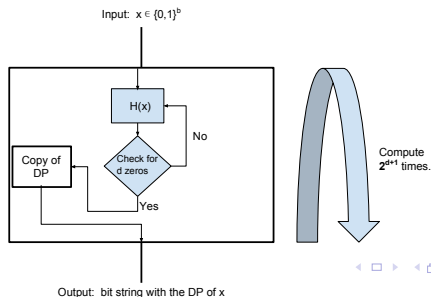
time 0:	x	y	0	0	0
time 1:	x	y	$H(x)$	0	0
time 2:	x	y	$H(x)$	$H^2(x)$	0
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time 4:	x	$y + H^3(x)$	$H(x)$	0	0
time 5:	x	$y + H^3(x)$	0	0	0

Distinguish point in quantum setting

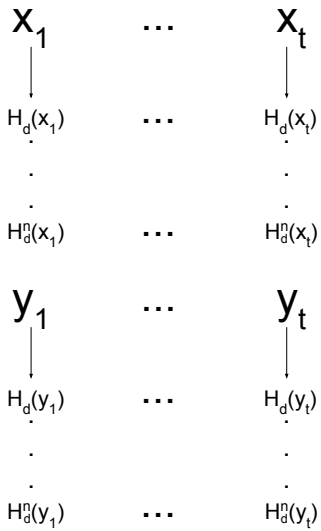
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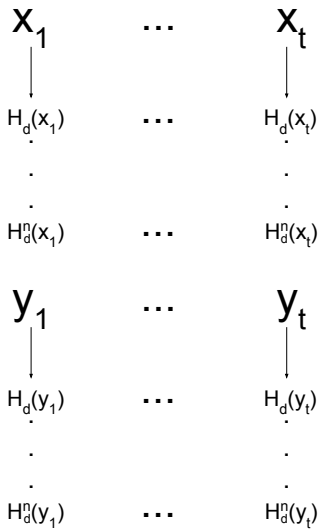
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time 4:	x	$y + H^3(x)$	$H(x)$	0	0
time 5:	x	$y + H^3(x)$	0	0	0



$$\begin{array}{ccc}
 \mathbf{X}_1 & \dots & \mathbf{X}_t \\
 \downarrow & & \downarrow \\
 H_d(x_1) & \dots & H_d(x_t) \\
 \cdot & & \cdot \\
 \cdot & & \cdot \\
 H_d^0(x_1) & \dots & H_d^0(x_t)
 \end{array}$$





$$H_d^n(y_i) \stackrel{?}{=} H_d^n(x_j)$$

Reversibility

Reversibility of distinguish point

- ▶ Bennett-Tompa technique to build a reversible circuit for H^n ;
- ▶ It is possible to achieve $a + O(b \log_2 n)$ ancillas and gate depth $O(gn^{1+\epsilon})$.

³Efficient distributed quantum computing

Beals, Robert and Brierley, Stephen and Gray, Oliver and Harrow, Aram W. and Kutin, Samuel and Linden, Noah and Shepherd, Dan and Stather, Mark

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Reversibility of sorting on a mesh network

- ▶ Using the sorting strategy from “Efficient distributed quantum computing”³;
- ▶ We used Odd-even mergesort;
- ▶ It is possible to perform the sorting of t elements using $O(t(b + (\log t)^2))$ ancillas and $O(t^{1/2}(\log t)^2)$ steps.

³Efficient distributed quantum computing

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Fix images y_1, \dots, y_t . We build a reversible circuit that performs the following operations:

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- ▶ If there is a collision, say a collision between the chain end for x_i and the chain end for y_j : recompute the chain for x_i , checking each chain element to see whether it is a preimage for y_j .
- ▶ Output 0 if a preimage was found, otherwise 1.

Example

- ▶ Imagine a function $H : \{0, 1\}^{40} \rightarrow \{0, 1\}^{40}$;

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- ▶ Consider $t = 2^8$ and $p = 2^8$, for this example.

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- ▶ Consider $t = 2^8$ and $p = 2^8$, for this example.
- ▶ The probability to find one preimage is roughly $t^{5/2}/N = (2^8)^{5/2}/(2^{40}) \approx 2^{-20}$;
- ▶ Each processor is going to use $\sqrt{N/pt^{3/2}}$ iterations;
 $\sqrt{2^{40}/2^8((2^8)^{3/2})} = \sqrt{2^{40}/2^{20}} = 2^{10}$ iterations.
- ▶ Overall, we get $(2^8)^{1/4}$ speedup from attacking 2^8 targets.

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- ▶ Each processor is going to use $\sqrt{N/pt^{3/2}}$ iterations;
- ▶ $\sqrt{2^{128}/2^{30}(2^{30})^{3/2}} \approx \sqrt{2^{128}/2^{75}}$
- ▶ $= \sqrt{2^{53}} \approx 2^{26}$ iterations.

Conclusion & What's next?

Conclusion:

- ▶ Circuit uses $O(a + tb + t(\log t)^2)$ ancillas;
- ▶ Depth of $O(\sqrt{N/pt^{1/2}}(gt^{\epsilon/2} + (\log t)^2 \log b))$;
- ▶ Approximately $\sqrt{N/pt^{3/2}}$ iterations.
- ▶ Created the circuit using quantum simulator for AES⁴ (libquantum instead of LiQUi |>);

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Grassl, Markus and Langenberg, Brandon and Roetteler, Martin and
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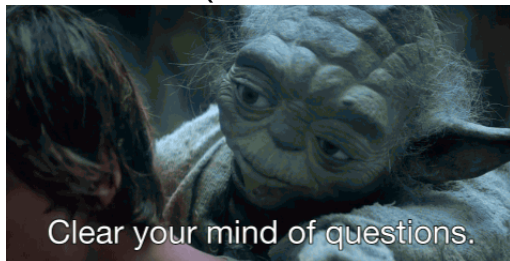
What's next?

- ▶ Check for the real number of qubits/gates;
- ▶ Is it possible to improve?

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Questions

Thank you for your attention.
Questions?



Clear your mind of questions.

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